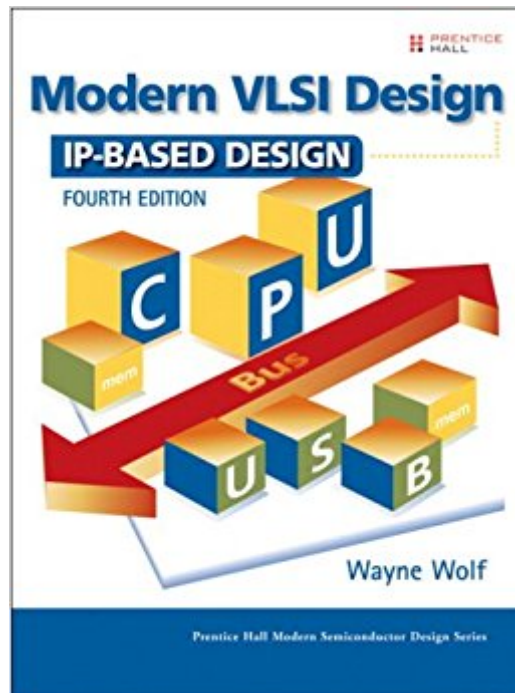




Ebook Directory
the best source of ebook

The book was found

Modern VLSI Design: IP-Based Design (4th Edition)



Synopsis

The Number 1 VLSI Design Guide – Now Fully Updated for IP-Based Design and the Newest Technologies Modern VLSI Design, Fourth Edition, offers authoritative, up-to-the-minute guidance for the entire VLSI design process – from architecture and logic design through layout and packaging. Wayne Wolf has systematically updated his award-winning book for today's newest technologies and highest-value design techniques. Wolf introduces powerful new IP-based design techniques at all three levels: gates, subsystems, and architecture. He presents deeper coverage of logic design fundamentals, clocking and timing, and much more. No other VLSI guide presents as much up-to-date information for maximizing performance, minimizing power utilization, and achieving rapid design turnarounds. Coverage includes – All-new material on IP-based design Extensive new coverage of networks-on-chips New coverage of using FPGA fabrics to improve design flexibility New material on image sensors, busses, Rent's Rule, pipelining, and more Updated VLSI technology parameters reflecting the latest advances Revised descriptions of HDLs and other VLSI design tools Advanced techniques for overcoming bottlenecks and reducing crosstalk Low-power design techniques for enhancing reliability and extending battery life Testing solutions for every level of abstraction, from gates to architecture Revamped end-of-chapter problems that fully reflect today's VLSI design challenges Wolf introduces a top-down, systematic design methodology that begins with high-level models, extends from circuits to architecture, and facilitates effective testing. Along the way, he brings together all the skills VLSI design professionals will need to create tomorrow's state-of-the-art devices.

Book Information

Hardcover: 656 pages

Publisher: Prentice Hall; 4 edition (December 31, 2008)

Language: English

ISBN-10: 0137145004

ISBN-13: 978-0137145003

Product Dimensions: 7.3 x 1.4 x 9.6 inches

Shipping Weight: 2.4 pounds (View shipping rates and policies)

Average Customer Review: Be the first to review this item

Best Sellers Rank: #1,111,954 in Books (See Top 100 in Books) #43 in Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > VLSI & ULSI #192

inÃ Â Books > Engineering & Transportation > Engineering > Electrical & Electronics > Electronics
> Semiconductors #228 inÃ Â Books > Computers & Technology > Programming > Software
Design, Testing & Engineering > Logic

Customer Reviews

The Number 1 VLSI Design Guide--Now Fully Updated for IP-Based Design and the Newest Technologies "Modern VLSI Design, Fourth Edition, " offers authoritative, up-to-the-minute guidance for the entire VLSI design process--from architecture and logic design through layout and packaging. Wayne Wolf has systematically updated his award-winning book for today's newest technologies and highest-value design techniques. Wolf introduces powerful new IP-based design techniques at all three levels: gates, subsystems, and architecture. He presents deeper coverage of logic design fundamentals, clocking and timing, and much more. No other VLSI guide presents as much up-to-date information for maximizing performance, minimizing power utilization, and achieving rapid design turnarounds. Coverage includes All-new material on IP-based designExtensive new coverage of networks-on-chipsNew coverage of using FPGA fabrics to improve design flexibilityNew material on image sensors, busses, Rent's Rule, pipelining, and moreUpdated VLSI technology parameters reflecting the latest advancesRevised descriptions of HDLs and other VLSI design toolsAdvanced techniques for overcoming bottlenecks and reducing crosstalkLow-power design techniques for enhancing reliability and extending battery lifeTesting solutions for every level of abstraction, from gates to architectureRevamped end-of-chapter problems that fully reflect today's VLSI design challengesWolf introduces a top-down, systematic design methodology that begins with high-level models, extends from circuits to architecture, and facilitates effective testing. Along the way, he brings together all the skills VLSI design professionals will need to create tomorrow's state-of-the-art devices.

Wayne Wolf is Rhesa Ã¢âÃ¢â"RayÃ¢âÃ¢â S. Farmer Jr. Distinguished Chair in Embedded Computing Systems and Georgia Research Alliance Eminent Scholar at the Georgia Institute of Technology. Before joining Georgia Tech, he was with Princeton University from 1989 to 2007 and AT&T Bell Laboratories from 1984 to 1989. He received the B.S., M.S., and Ph.D. degrees in electrical engineering from Stanford University in 1980, 1981, and 1984, respectively. His research interests include VLSI systems, embedded computing, cyber-physical systems, and embedded computer vision. He has chaired several conferences, including CODES, EMSOFT, CASES, and ICCD. He was founding editor-in-chief of ACM Transactions on Embedded Computing Systems and

founding co-editor-in-chief of Design Automation for Embedded Systems. He is a Fellow of the ACM and IEEE. He received the ASEE/CSE and HP Frederick E. Terman Award in 2003 and the IEEE Circuits and Systems Education Award in 2006.

[Download to continue reading...](#)

Modern VLSI Design: IP-Based Design (4th Edition) VLSI DESIGN SIMPLE AND LUCID

EXPLANATION: vlsi design for students Circuits, Interconnections, and Packaging for Vlsi

(Addison-Wesley VLSI systems series) CMOS VLSI Design: A Circuits and Systems Perspective

(4th Edition) Fundamentals of Modern VLSI Devices CMOS VLSI Design: A Circuits and Systems

Perspective (3rd Edition) Modern Essentials Bundle 6th - Modern Essentials 6th Edition a

Contemporary Guide to the Therapeutic Use of Essential Oils, An Introduction to Modern Essentials,

and Modern Essentials Reference Card VLSI Memory Chip Design (Springer Series in Advanced

Microelectronics) (v. 5) [Digital VLSI Chip Design with Cadence and Synopsys CAD Tools] By

Brunvand, Erik (Author) [2009] [Paperback] Digital VLSI Chip Design with Cadence and

Synopsys CAD Tools VLSI Physical Design Automation: Theory and Practice Algorithms for VLSI

Design Automation Algorithms for VLSI Physical Design Automation CMOS VLSI Design: A Circuits

and Systems Perspective Using The Electric VLSI Design System Nanoscale CMOS VLSI Circuits:

Design for Manufacturability Chip Design for Submicron VLSI: CMOS Layout and Simulation VLSI

Digital Signal Processing Systems: Design and Implementation VLSI Test Principles and

Architectures: Design for Testability (The Morgan Kaufmann Series in Systems on Silicon) VLSI

Physical Design: From Graph Partitioning to Timing Closure

[Contact Us](#)

[DMCA](#)

[Privacy](#)

[FAQ & Help](#)